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The TPS51200 device is a sink and source double data rate (DDR) termination regulator specifically designed for low input voltage, low-cost, low-noise systems where ...s5 c6 sp-cap $2 \times 150 \mu\text{f}$ c2 ceramic $1 \mu\text{f}$ c5 $2 \times 10 \mu\text{f}$ pgood vref 0.9 v 10 ma vtt 0.9 v 2 a tps51116 20 19 18 17 vbst drvh ll drvl v5filt vldoin vttgnd vttsns 7 8Supporting TDPs ranging from 17W to 35W, 2nd Generation AMD Embedded R-Series APUs equip system designers to achieve aggressive performance and energy efficiency ...Virtex 6 HXT FPGA Low Latency platform for accelerated trading and TCP offload